

FIG. 1

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FIG. 2 is a schematic diagram of an optical data throughput protection switch system. The system includes an amplifier bay 62, an amplifier circuit pack 60, and other optics 21. The amplifier circuit pack 60 includes an optical data throughput protection switch 20, a test port 14, and a connector 42. The test port 14 is connected to test equipment (OTDR) 50. The system also includes a splice 114, a splice 113, a splice 122, and a splice 120. The system is connected to a test equipment (OTDR) 50 via a connector 42. The system is connected to a test equipment (OTDR) 50 via a connector 42.

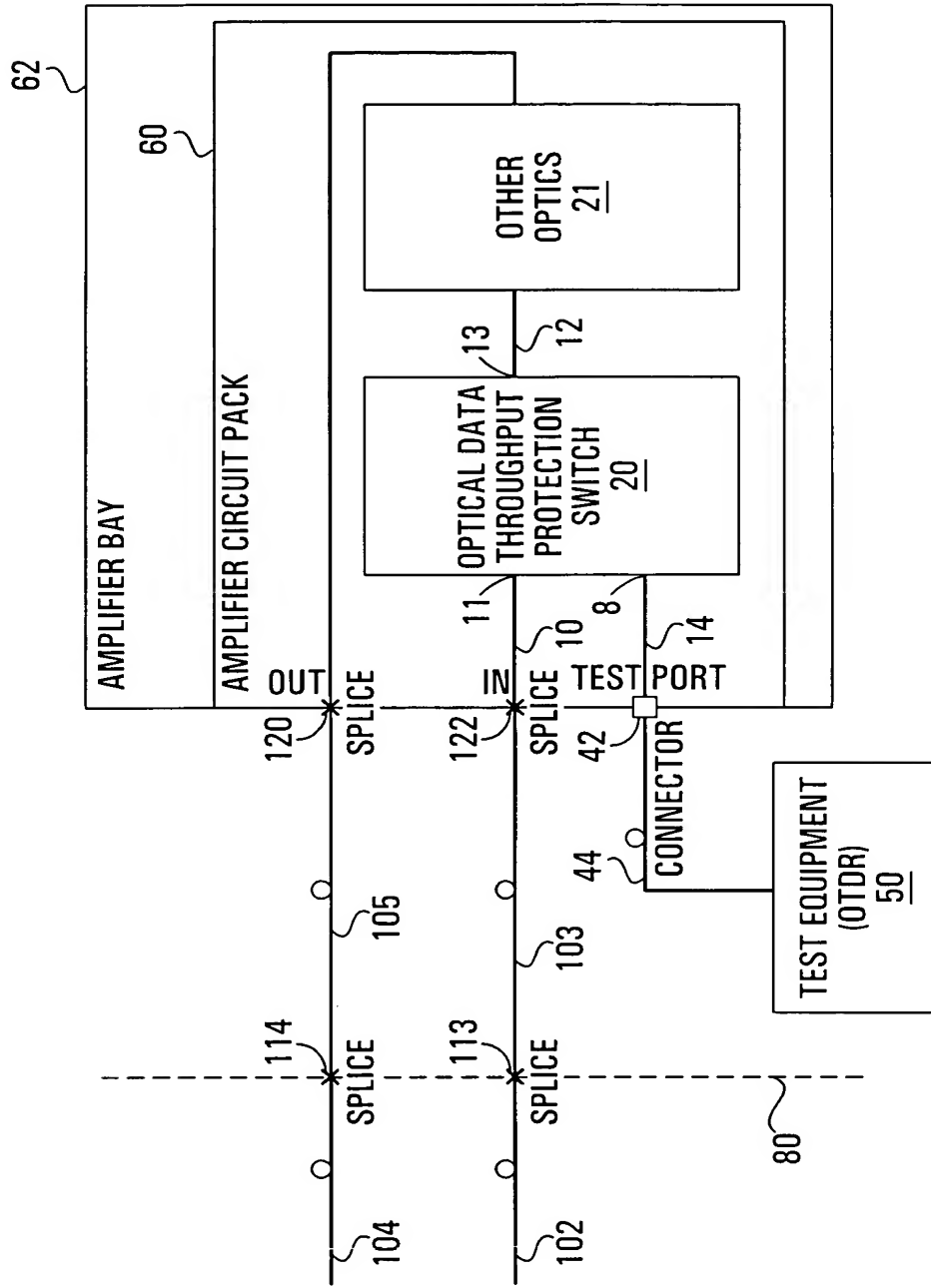


FIG. 2

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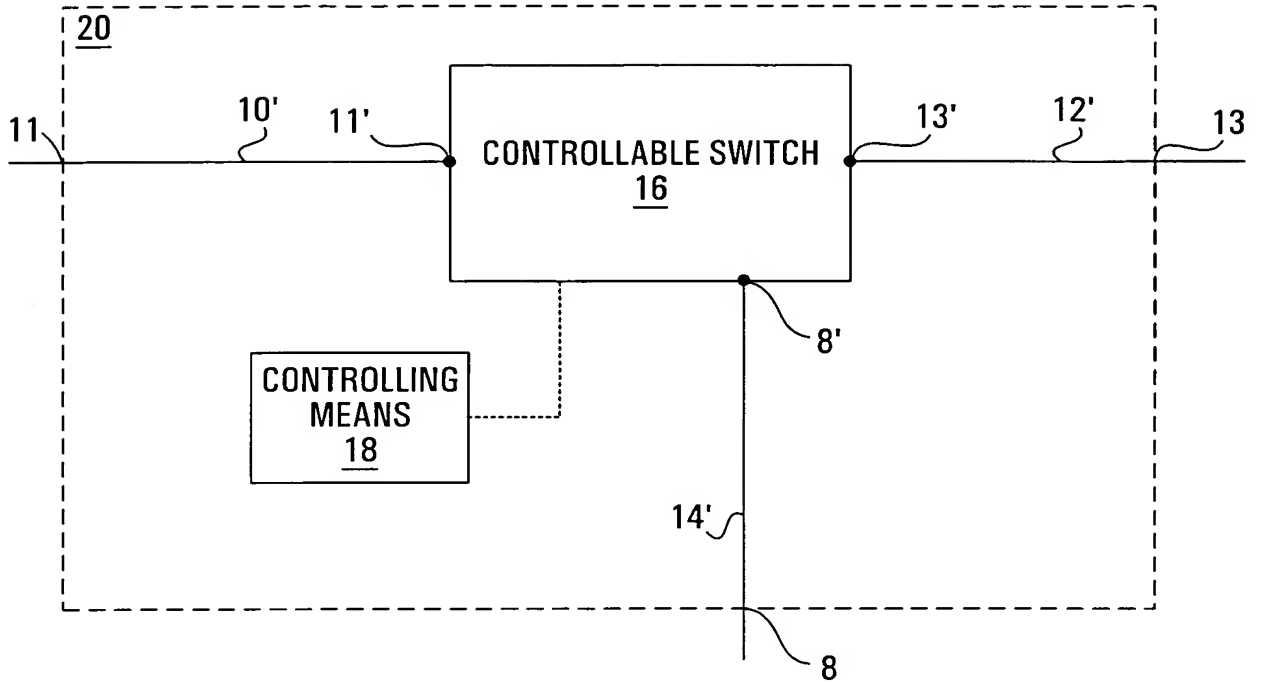


FIG. 3

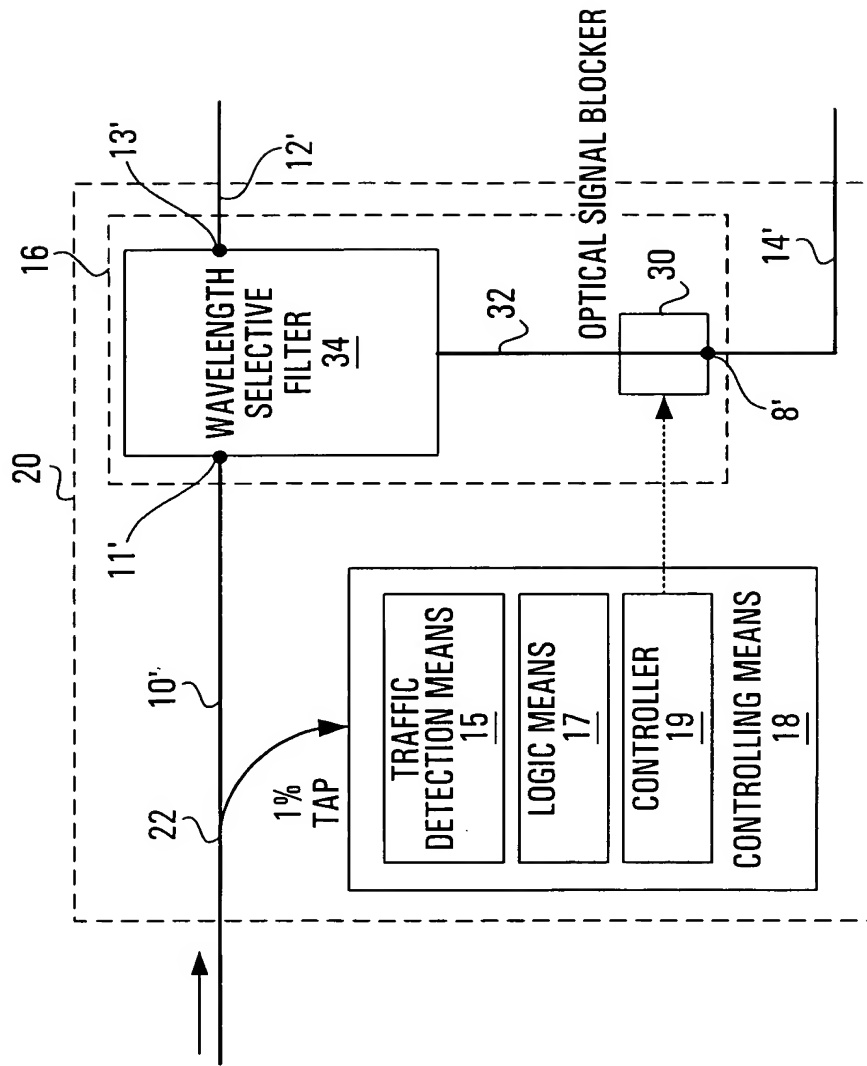


FIG. 4

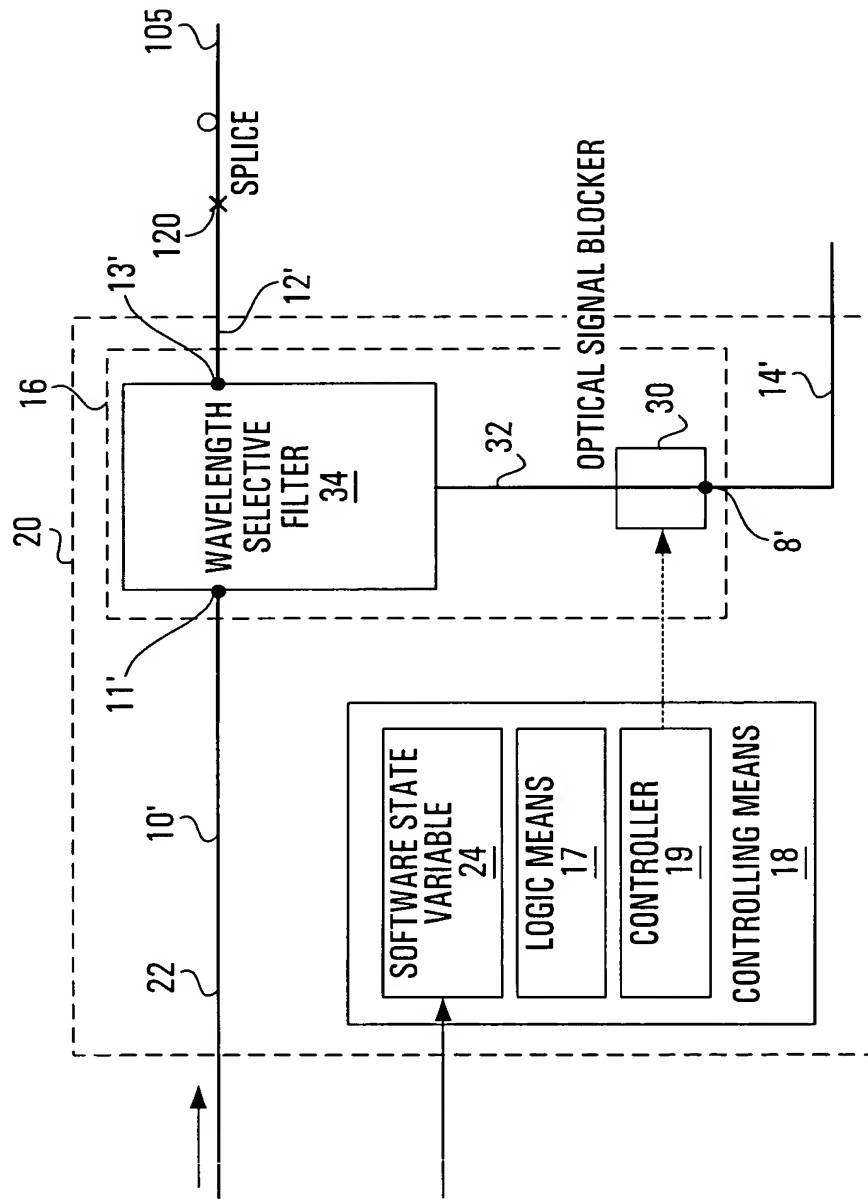


FIG. 5

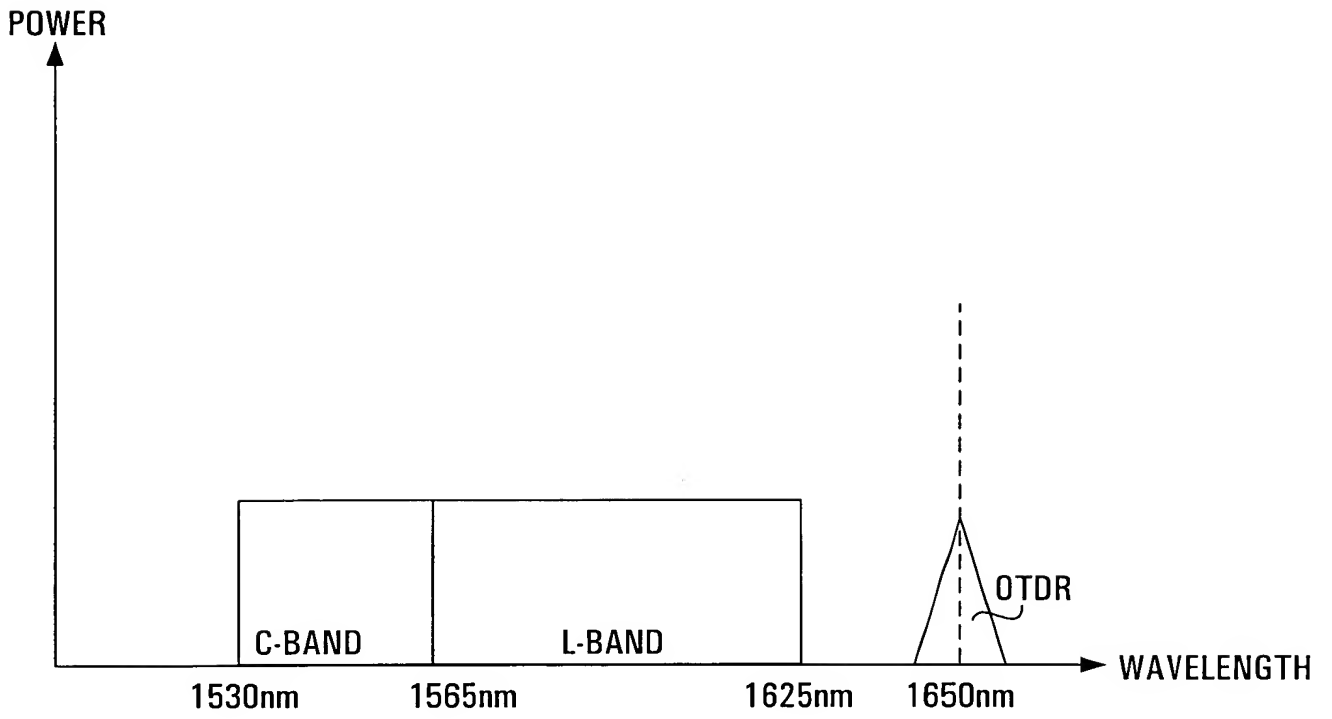


FIG. 6